

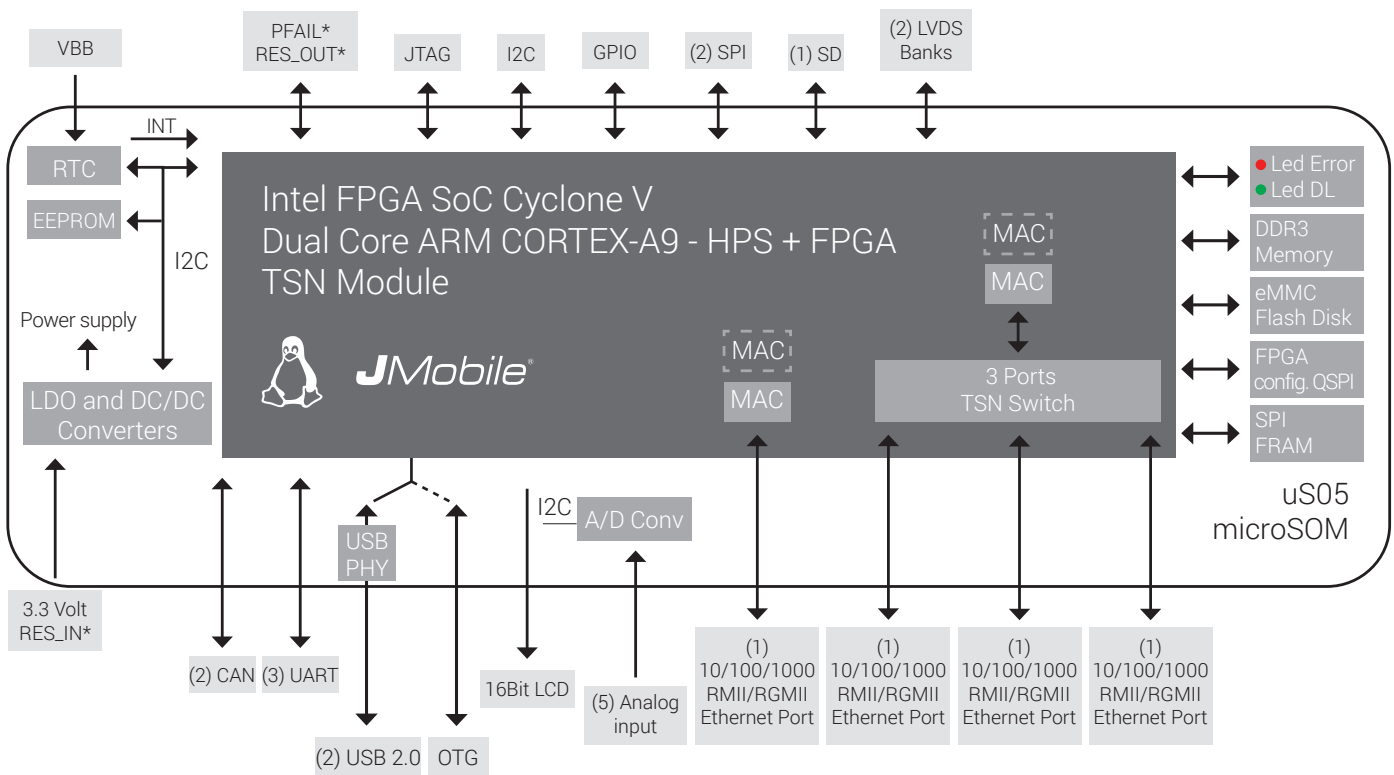
# microSOM uS05 TSN Version - Technical Data



microSOM uS05 Intel FPGA now support new Time-Sensitive Networking (TSN) mechanisms. Devices using this product have successfully completed interoperability tests in the renowned Industrial Internet Consortium TSN testbed.

Dimensions	46x35 mm
Temperature Range	-40°C to +85°C
CPU	Intel 5CSEBA6 Dual ARM Cortex - A9 925 MHz Integrated with FPGA 110 KLE
DDR	1 Gbyte DDR3
Flash Disk / QSPI	4 GByte eMMC / 256 Mbits
EEPROM	4 Kbits
FRAM	64-Kbytes
Watchdog/RTC/Voltage monitor/JTAG	Yes
USB	2 (Host V2.0) or 1 (OTG)
Ethernet	4 (RGMII 10/100/1000 Mb Ethernet MAC)
TSN	3 Ports Ethernet TSN Switch (RGMII)
SD	1
Serial Port	3
SPI	2
I2C	1
CAN	2
Video	1 (Video Out Controller 16 Bit)
Analog Input	5
GPIO	Several GPIO available depending of FPGA configuration, 2 LVDS I/O with 5 lanes each

Software configurable. Not all selections can be combined



## Ordering Information

Model	Part Number	Description
uS05-0001	+US05-0001	Cyclone V SoC - 110 KLE - 1GB DDR3 - 4 GB Flash Disk - TSN Support - Operating Temp. -40 to +85°C
uS05 TSN Kit	+EE16EK-0008	microSOM uS05 Development Kit

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